



A Report of workshop on VLSI Chip Design

(21 jan 2017)

Work shop on “VLSI Chip Design” **was** held from 21 jan 2017 organized by ECE Department prakasam engineering college, kandukur, where students of Engineering, and Management Sciences were present. The main objective of the workshop to have a gander at present educational framework and to give the quality and quantity output with the help of the some educational modules structures. **Mr.P.Murali Krishna**, Prof in ece department QITS,markapur, was invited by the institution as speaker for two days work shop.

The event started on 21 jan 2017 with an overwhelmingly warm welcome address delivered by Dr.Ravikumar, followed by words of motivation by Dr. LAKSHMANA RAO, Principal of PEC, welcomed our resource person **Mr.P.Murali Krishna**, Prof in ece department QITS,markapur. One of the students, sang a melodious rendition to invoke the blessings of the Goddess of Enlightenment. The other students were inspired by the motivating words of Dr. LAKSHMANA RAO followed by the interesting talk by **Mr.P.Murali Krishna**.

Mr.P.Murali Krishna, conducted the workshop with power point presentation. This workshop could help the students to a great extent in respect of their doubts in their respective fields, as number of students are facing lots of problems in the field of VLSI Chip Design. This workshop helped the students to enhance their knowledge in the area of VLSI Chip Design as these days there are lots of cases of VLSI Chip Design. We are quite sure that this interactive workshop would make the students aware in this particular segment .

At the end of the programme, few students gave their feedback, how they were benefited. At the end **Mr.P.Murali Krishna**, was presented with token of appreciation by Principal Dr. LAKSHMANA RAO for attending this programme.